AMENDMENTS TO THE SPECIFICATION:

Please amend the indicated paragraphs of the specification in accordance with the amendments indicated below.

Page 24, after the second full paragraph, insert:

FIG. 27 is a schematic diagram showing a low voltage bank parallel to a high-voltage bank wherein each bank is comprised of lower-capacity configuration banks.

Page 30, after the first full paragraph and before the heading "(Method Example 1)," insert:

Referring to Fig. 27 a high level schematic of the first apparatus example is presented wherein components discussed above are shown. The high-voltage load system circuit **ε** includes the low-voltage bank **LB** which is composed of a plurality of the lower-capacity configuration banks formed of the low-voltage resistor circuits **βb** each formed of a plurality of the low-voltage three-phase resistor circuits **αb** connected via the plurality of the switches **S5-S7** in parallel to an output terminal of a transformer input terminal (not shown) which is in turn connected to the central breaker **CB**. The high-voltage bank **HB** includes of a plurality of lower-capacity

configuration banks formed of the high-voltage resistor circuits **βa** each of which includes a plurality of the high-voltage three-phase resistor circuits **αa** connected to the switches **S1-S4**. The low-voltage bank **LB** and the high-voltage bank **HB** are thus connected in parallel to the high-voltage power generator **GR** through the central breaker CB. The low-voltage three-phase resistor circuits **αb** and the high-voltage three-phase resistor circuits **αa** each are comprised of resistor arrays in three phases, each of the resistor arrays being comprised of resistor elements **1** connected in series, as discussed above, in a form of a Y-connection in which three of the resistor arrays are concentrated for reconciliation of their phases so that an isolated and independent neutral point unconnected to those of the other three-phase resistor circuits is formed as discussed above. Alternatively, the resistor arrays are connected in a form of a Δ-connection in which each terminal of the resistor arrays in three phases is connected to each of in-phase branch distribution lines of a power cable as discussed above.